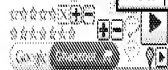




breakpoint pipeline stage (screen OR gui OR window OR display)

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Show options...

1. [Model Debugger for Fast Models User Guide: 3.5.2. Pipeline Table ...](#)

Pipeline Table window The **Pipeline Table** shown in gives a very detailed view of the **pipeline stages**. By default, the table shows views for all **pipeline stages**. ... This is the same dialog that is available from the **Breakpoint Manager** dialog. ... Select to **display** a submenu for the **Pipeline Table**. See Figure 3.37. ...

infocenter.arm.com/help/topic/com.arm.doc.../ch03s05s02.html - [Cached](#)

2. [WinDLX Tutorial](#)

Jump to **Breakpoint window**: This is a system call to write to the **screen**. ... A **window** will inform you that "ID-Stage: reached at **Breakpoint #1**" ... The reason for this is that the **pipeline** is cleared in DLX whenever ... You can check this by clicking on Execute / **Display** DLX-I/O in the main **window's** menu line. ...

www.ndsu.nodak.edu/instruct/tareskiree774196/.../wldxtut.htm - [Cached](#) - [Similar](#)

3. [Method and apparatus for providing chained breakpoints in a ...](#)

breakpoint state machine circuitry connected to the point of no return instruction **pipeline stage**, operable to monitor **breakpoint** tags received by the point ...

www.patentstorm.us/patents/6449736/claims.html

4. [WinDLX and IMIPSim Pipeline Simulators for Teaching Computer ...](#)

Breakpoint Window. Setting **breakpoints** stops the simulation at user defined points. ... The instruction content of the different **pipeline stages** is ...

doi.ieeecomputersociety.org/10.1109/ECBS.1996.494568 - [Similar](#)

by H Gruenbacher - 1996 - [Cited by 12](#) - [Related articles](#) - [All 4 versions](#)

5. [\[PDF\] WinDLX Tutorial](#)

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The exact appearance of your **screen** cannot be foretold (e. g. that only the first

pipeline stage, IF, has been executed and then the ... **breakpoints** (only one so far) is shown. Re-Iconize the **window** again. ... **Display** DLX-I/O in the main **window's** menu line. The created **window** shows you the **screen's** appearance. ...

www.it.uu.se/edu/course/homepage/dark/tit03-mm1/.Jwdlxut.pdf - [Similar](#)

6. [Simulator Tutorial](#)

Setting Instruction, Data, Memory, and Register **Breakpoints** The format of the **display** is similar to the memory **window** in MSVC. ... Displays the names of all the **pipelines** and the names of all the **pipeline stages** within each **pipeline**. Print a prompt to the **screen** and read a single character from the ...

www.cmc.minois.edu/~awise/SimulatorTutorial.htm - [Cached](#) - [Similar](#)

7. [\[PDF\] Optimizing C55x Assembly Code Using the Pipeline Stall Analyzer Tool](#)

File Format: PDF/Adobe Acrobat - [Quick View](#)

pipeline stage after the read of the register, then the **pipeline** Set a **breakpoint** at the beginning of the assembly code section to analyze. ... Open the IBQ **window** by checking the "Show IBQ" box in the **Pipeline Analyzer window**. ...

focus.ti.com/tit/anspra865/spra865.pdf

8. [Pipeline buffer between stages? - Intel® Software Network](#)

18 posts - 5 authors - Last post: Nov 9

1) Should I place a buffer between each **stage** of the **pipeline**? ... what about using data-dependent **breakpoints** in a debugger to find out which ... individual txt files. one for each **stage** instead printing to the **screen**. ...

software.intel.com/en-us/forums/intel-threading-building.../69489/ - [Cached](#)

9. [\[PDF\] WINDLX](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Pressing F7 again will re-arrange the colours in the code **window**, introducing red for the third **pipeline stage** intEX. ... **Breakpoint Window**. • To put a **breakpoint** in correspondence of an ... Clicking on Execute / **Display** DLX-I/O in the main **window's** menu line, a **window** shows the **screen's** appearance. ...

www.cad.polito.it/~sonza/01gol/lucid/aa2003-04/WIN_DLX.pdf - [Similar](#)

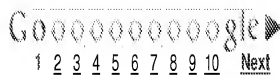
10. [\[PDF\] Teaching Computer Architecture/Organisation using simulators](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Breakpoint, Register and Statistics **Window** ... The instruction content of the different **pipeline stages** is displayed on top of each **stage**. ... on the right side of the windows main **screen** shows the main components of the simulator. ... the **display** of the instruction. Clicking on the Queue, Register and Data Cache ...

file-conference.org/files98/papers/1380.pdf

by H Grünbacher - 1998 - Cited by 19 - Related articles



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